HITACHI INVERTER

J100-E2 SERIES

SERVICE MANUAL (ADJUSTMENT AND MAINTENANCE)

Model: J100-004SFE2 to J100-022SFE2

J100-015HFE2 to J100-037HFE2

After reading this manual, keep it at hand for future reference.

NBS470XB

Hitachi, Ltd. Tokyo Japan

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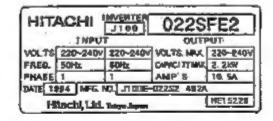
Before starting adjustment and maintenance, be sure to check the following specifications of the inverter and motor.

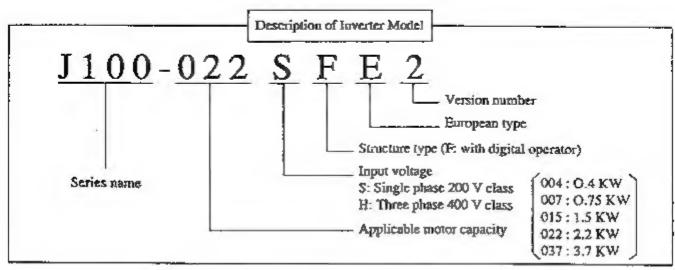
1.1 Check of the inverter model name and manufacturing No.

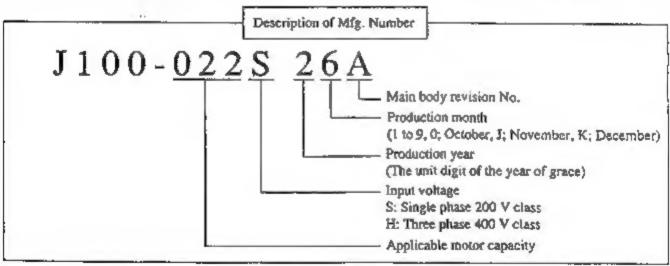
Inverter model	J100	This information is written
MFG. NO.		on the name plate on the side cover of the inverter

Example of contents of specification label

The example is for the J100 022SF E2 invener model







1.2 Check of inverter and motor specifications

(1) Inverter specifications

Monitor Mode

NO.	Monitor name	Display content	Set value
1	Frequency setting	FS000.0 000.0Hz	
	and output frequency	1S005.0 000.0Hz	
	Multistage speed	2S020.0 000.0Hz	
	setting and output frequency	3S040.0 000.0Hz	
	(4S000.0 000.0Hz	
	Expansion multistage	5S000.0 000.0Hz	
	speed	6S000.0 000.0Hz	
	(7S000.0 000.0Hz	
2	Acceleration time setting	ACCEL-1 0010.0S	
3	Deceleration time setting	DECEL-1 0010.0S	
4	2-stage acceleration time setting	ACCEL-2 0010.0S	
5	2-stage deceleration time setting	DECEL-2 0010.0S	
6	Frequency setting command	F-SET-M Terminal	
7	Operation command method	F/R-SW Terminal	
8	Revolution speed display	RPM 4P 00000RPM	-
9	Output current display	If A Im000.0%	
10	DC current display	PN-V 000V	
11	Output voltage gain adjustment	V-Boost Code <31>	
12	Output voltage gain adjustment	V-Gain 100%	
13	Analog meter adjustment	M-ADJ 50	
14	Failure display	#	
		?ERROR Over V.	In case of over voltage tripping
15	Failure history	PERR COUNT 000	

Function Mode

		Function name	Standard setting	Set value
F-00	V/F pate	ern setting	V/F-VC 050-050	
F-01	Maximu	m frequency adjustment	0	
	Start fre	quency adjustment	0.5	
F-03	Maximu	m frequency limiter setting	0	
F-04	Minimu	m frequency limiter setting	0	
7-05	Multista	ge-speed first speed setting	0 (Hz)	
F-06	Multista	ge-speed second speed setting	0 (Hz)	
3-07_	Multista	gc-speed third speed setting	0 (Hz)	
7-08	Multista	ge-speed fourth speed setting	0 (Hz)	
09	Multista	ge-speed fifth speed setting	0 (Hz)	
-10	Multista	ge-speed sixth speed setting	0 (Hz)	
<-11 J	Multista	ge-speed seventh speed setting	0 (Hz)	
		ine frequency adjustment	0.5 (Hz)	
7-13	DC brok	ing force adjustment	0	
2 1 4	Sleetros	ic thermal level adjustment	0 (S) 100 (%)	
		ntion selection(Lincar, Curve)	Lincar	
		ntion selection(Linear, Curve)	Lincar	
7-18	External	frequency setting start	0 (Hz)	
7-19	External	frequency setting end	0 (Hz)	-
-20		Set DC braking	DCB OFF	
		Switch over of frequency monitor	FM ANA	
		Switch over of the maximum frequency	fmax 120	
	Switch	Switch over of trip and retry	PWERALM	
	selec-	Switch over of the motor direction when using the	DIOP FWD	
	tion I	digital operator		
		Direction of the motor (Forward)	FWD ON	_
		Direction of the motor (Reverse)	REV ON	
201		Overload limiter	OLMT ON	
-21	Switch selec- tion 2	DC braking edge/level selection	DB TAT	
		Stop key is effective when external run is selected	STOP ON	
- 1		Selection of electronic thermal characteristic Selection of electronic thermal operation	Ethns 100	
		Selection of software lock	Ethm ON SLOK ON	
1		Setting voltage for analog input	AIN 5V	
- 1		Setting input method of analog input	AIN VOL	
		Selection of analog input	AIN TER	
-22	.,	Change of data	SOFTFREE	
		P-1		
- 1	1	Selection of frequency arrival	FARV 2	
	Switch	Selection of neglect of trip	TRIP OFF	
	Switch selec-	Selection of neglect of trip Debug mode display	TRIP OFF DEBG OFF	
	Switch selec- tion 3	Selection of neglect of trip Debug mode display Software lock	TRIP OFF DEBG OFF TLOK ON	-
	selec-	Selection of neglect of trip Debug mode display Software lock Selection of area	TRIP OFF DEBG OFF TLOK ON AREA EC	
	selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD	
192	selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT	
7-23	selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1	
-23	selec- tion 3	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2	TRIP OFF DEBG OFF TLOK ON ARÉA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2	
7-23	Switch selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3	TRIP OFF DEBG OFF TLOK ON ARÉA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH	
-23	selec- tion 3	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH TER4 R\$	
-23	Switch selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH TER4 R\$ TER5 FM	
	Switch selec-	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR	
-24	Switch selec- tion 4	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A	
-24	Switch selec- tion 4	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A	
-24	Switch selec- tion 4	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN 1	
-24	Switch selec- tion 4 Switch selec- tion 5	Selection of neglect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A or B	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CFI TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN I AVR ON LAD ON ALM B	
-24	Switch selec- tion 4 Switch selec- tion 5	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A or B limiter constant	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN I AVR ON LAD ON ALM B 150% 1.0	
-24 -25 -26	Switch selec- tion 4 Switch selec- tion 5 Overload Allowab	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B limiter constant le undervoltage time	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN I AVR ON LAD ON ALM B 150% 1.0	
-24 -25 -26 -27	Switch selec- tion 4 Switch selec- tion 5 Overload Allowab Stand by	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B limiter constant le undervoltage time time after undervoltage setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN 1 AVR ON LAD ON ALM B 150% 1.0	
-24 -25 -26 -27 -28	Switch selection 4 Switch selection 4 Overload Allowab Stand by DC brak	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B limiter constant le undervoltage time time after undervoltage setting ing usage ratio setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN I AVR ON LAD ON ALM B 150% 1.0 001.0S 0010.0S	
-24 -25 -26 -27 -28 -29	Switch selection 4 Switch selection 4 Switch selection 5 Overload Allowab Stand by DC brak Frequence	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B Ilimiter constant le undervoltage time time after undervoltage setting ing usage ratio setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN 1 AVR ON LAD ON ALM B 150% 1.0 001.0S 0010.0S 5.0% ACC. DEC 100%	
-25 -26 -27 -28 -29 -30	Switch selection 4 Switch selection 4 Switch selection 5 Overload Allowab Stand by DC brak Frequenc Carrier for	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B Ilimiter constant le undervoltage time time after undervoltage setting ing usage ratio setting requency setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN 1 AVR ON LAD ON ALM B 150% 1.0 001.0S 0010.0S 5.0% ACC. DEC 100%	
-24 -25 -26 -27 -28 -29 -30 -31	Switch selection 4 Switch selection 5 Overload Allowab Stand by DC brak Frequenc Carrier filingut vol	Selection of nepiect of trip Debug mode display Software lock Selection of area Whole data setting Trip history clear Terminal setting 1 Terminal setting 2 Terminal setting 3 Terminal setting 4 Terminal setting 5 Terminal setting 6 External trip input selection RUN signal output selection AVR value selection for deceleration LAD stop function selection Selection of alarm output contact A of B Ilimiter constant le undervoltage time time after undervoltage setting ing usage ratio setting	TRIP OFF DEBG OFF TLOK ON AREA EC FUNC STD TCNT CNT TERI CF1 TER2 CF2 TER3 2CH TER4 RS TER5 FM TER6 AR EXT A RUN 1 AVR ON LAD ON ALM B 150% 1.0 001.0S 0010.0S 5.0% ACC. DEC 100%	

Output voltage - Output frequency Hz Motor mfg, number kW Motor output Rated current Number of motor poles Rated voltage Rated frequency Motor rated rpm Starting frequency (3) Mating equipment specifications Required acceleration/deceleration time Equipment name Sec Acceleration time Torque characteristics Sec Deceleration time Required torque kg-m Hz Variable speed range Hz kg-m² Load GD2

(2) Motor specifications

2. FUNCTION OF CHECK TERMINALS (CN1 CONNECTOR)

Table 2.1 below shows the functions of the CN1 connector on the printed-circuit board. Refer to Sub-section 4.1 for the connector location and pin numbers.

Table 2.1 Functions of check terminals

CNI Connector Terminal symbol	Pin No.	Function	Waveform observation
PV5	CN1(12)	Digital circuit power supply PV5 ← GNDA: 4.9 to 5.2V	DC power supply
NV12	CN1 (2)	CT/Remote operator power supply NV12 ← GNDA: -10.8 to -15.0V	DC power supply Note: When the remote operator is connected
VDC	CN1 (1)	Main circuit DC voltage detection signal: When VPN is 300VDC (200V class), 600VDC (400V class) VDC ← GNDA: 24.3 to 25.9V	DC power supply Note: When the remote operator is connected
GNDA	CN1 (13) (14)	Reference voltage for the power supply above	
U V W X Y Z	CN1 (20) (19) (8) (18) (17) (7)	PWM waveform logic signal; Period during which the main circuit transistor is ON and OFF is shown. Measurement of nonlapped logic is possible by observing waveforms by the pairs, i.e., U and X phase, V and Y phase, and W and Z phase. Allowable nonlapped period range: t = 2 to 4µ sec.	U—GNDA Approx. 5 V O V Main transistor is ON during this period Main transistor is OFF thuring this period U — GNDA X—GNDA Nonlapped logic U t ₂
IU IW	CN) (5) (4)		1U, IW-GNDA 2V 2V

CN1 Connector Terminal symbol	Pin No.	Punction	Waveform observation
TRIP	CN1 (6)	IPM module protect detection signal (approx. 5 V) TRIP← GNDA 5 V (approx.) When IPM is tripped, "H" → "L"	DC voltage
PV24	CN1 (22)	Power supply to Fan and Power relay	
		PV ← GNDB 004 to 007SF 21.6 to 30.0V	DC voltage
		015 to 022SF 015 to 037HF 21.6 to 26.4V	
GNDB	CN1(11)	Reference voltage for PV24 power supply	

3. TROUBLE SHOOTING PROCEDURE

3.1 Failure messages and diagnoses

When the inverter is out of order, be sure to take the actions indicated on Table 3.1. Find and correct the cause of the trouble and then reuse.

- NOTE 1: When change of setting is required, be sure to obtain approval of the customer before changing.
- NOTE 2: When checking the inverter or making repairs because of the faulty condition, be sure to follow the instructions given in Section 4 through Section 7.

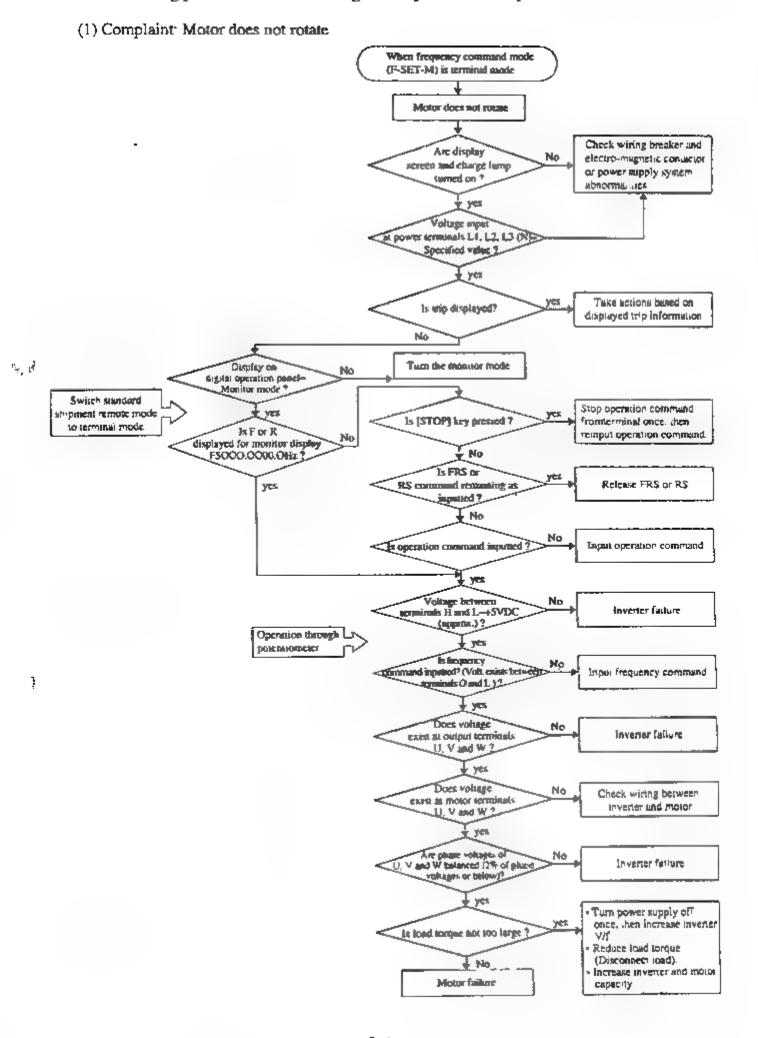
Table 3.1 Failure messages and diagnoses

·ular	Ph	enomeno			in a superior			** H All
Breaker MCB	Electromagnetic contactor Mg Thermal relay	LED di	operator	Fault alarm relay	Probable cause [Contents of message]	Method to reset	Checksfore	Correction
		E	D		Overcurrent during motor constant rpm operation	A	Rapid changes in load	Eliminate rapid changes in load
		(PM.	Drive)	ľ	(Overcurrent during operation)	n	Short-circuits or grounds of output	Check output wiring and motor for shorts
		F	2		Overcurrent Ouring motor		Rapid deceleration	Increase deceleration time
			Decel)		deceleration	A	Short-circuits or grounds of output	Check output wiring and motor for shorts
			- G		Overcurrent during motor acceleration	A	Rapid acceleration	Increase deceleration time
		_					Short-circuits or grounds of output	Check output wiring and motor for shorts
		_	Accel)	О			Too high starting frequency	Decrease starting frequency
		(FIVE	Accel)				Too high torque boost	Decrease torque boost
							Locked motor	Check motor or load
		E H O	0	Excessive temperature of main element (PM) while		Installed position (vertical) and wall surface (nonflam- mable material, such as steel plate)	Check installation	
	(motor is at rest. Faulty internal power supply		Cooling fan opera- tion and ambient temperature	Replace cooling fan	
					of the inverter		Internal power supply	Repairs

	Breaker MCB	contactor Mg	Thermal relay	Digital operator LED display [Remote operato LCD display]	Fault slarm relay	[Contents of message]	Method to reset	Click tol.	Correction
	ŀ			(Over, L)	O	Inverter over- loaded (Opera- tion under	A	Excessive load Electronic thermal level. (If not changed)	Decrease load factor Rematch to proper level
				(OL. BRD)	C	Regenerative brake applica- tion time exceeding the BRD%ED value setting	A	Damping resistance	Increase decelera- tion time Increase operating duty cycle Raise BRD%ED setting
								Rapid deceleration	Increase deceleration time
4.			3	E (Over. V)	C	DC smooth- ing circuit overvoltage	A	Motor forced to rotate by the load	Impossible to apply to continuous regenerative load
				(Over. v)			Grounding fault	Check output wiring and motor for grounds	
		•		FA		E² PROM		Large noise sources in proximity	Keep noise sources away
				(EEPROM)	C	Error	l I F	Ambient temperature (too high)	Replace cooling fan
								Decrease in voltage	Evaluate power supply system
				E 9		Faulty power		Faulty contact of MCB or Mg	Replace MCB or Mg
10				(Under. V)		supply (undervoltage)	A	Repeated occurrence of momentary power failure of 100msec or less by 10 times or more in 10 minutes.	Evaluate power supply system
				E 10 (CT)	C	CT Error	A	Faulty CT	Repairs

	44	Ph	enomenon , 5 , 7	`	ے		1	
Breaker MCB «	Electromagnetic contactor Mg		Digital operator LED display [Remote operator LCD display]	151	Probable cause [Contents of	Method to reset	Check for	Correction *
			611	0	(CPU Error)	A	Large noise sources in proximity	Keep noise sources away
	'		(CPU)				Faulty inverter	Repairs
			(EXTERNAL)	0	External trip	A	Faulty external device or equipment (When external trip function is selected)	Eliminate external device and equipment faulty conditions
			EI3 (USP)	0	USP Error	A	If power is not turned on with inverter in RUN state (When USP function is selected)	Turn on power with
			(GND. Fit)	0	Grounds at inverter output (When power is turned on)	A	Grounding of wiring between inverter and motor, and motor itself (Check with the megohin-meter)	Correct grounded portion
	1				-		Overload	Decrease load factor
		0		-	 -	C	Improper thermal relay set value	Reset to proper value
							Ground fault and shorts of power supply	Correct shorted or grounded portion
0				_		В	Undercapacity of MCB	Increase MCB capacity
							Damaged inverter module or converter module	Repairs
							Power failure	Correct power supply
	0			-	Power failure	B	Faulty contact of MCB or Mg	Replace MCB or Mg

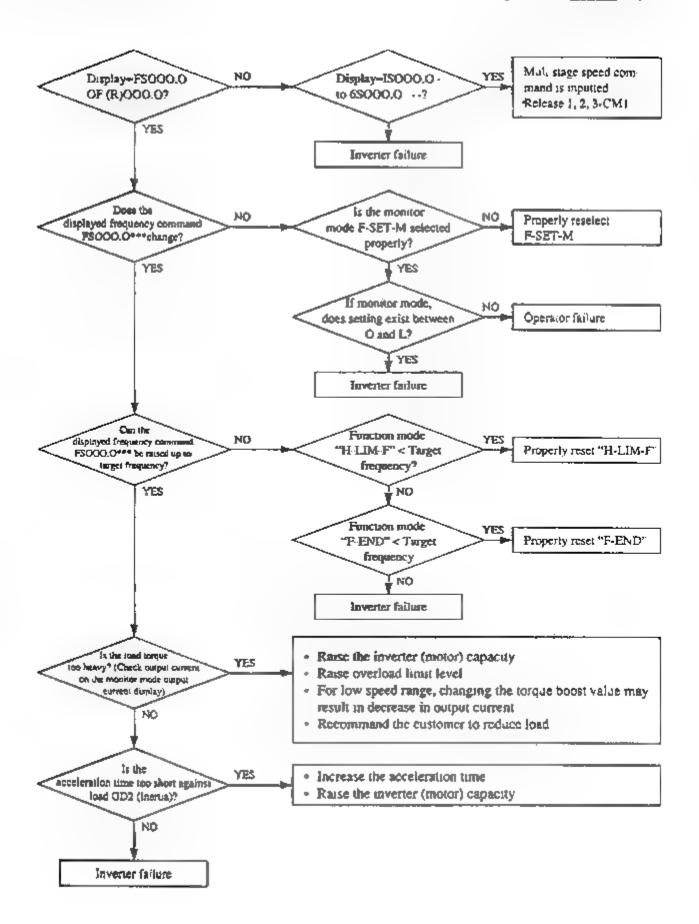
- 3.2 Description of the codes A, B and C under the tabulated column heading " Method to reset".
- A: After the motor has stopped, close between the terminals RS and L on the printed-circuit board, or press the stop/reset key of the main body digital operator.
- B: Operate the breaker and electromagnetic contactor.
- C: Reset the thermal relay after the motor has stopped.

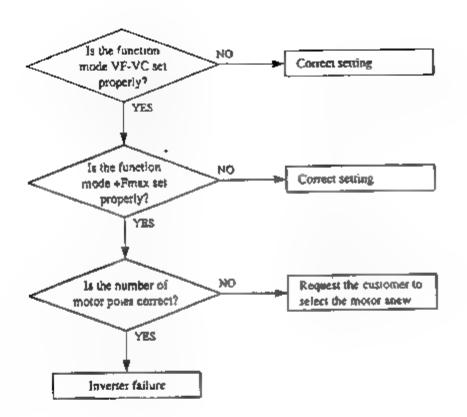


(2) Complaint: Motor does not accelerate

3.

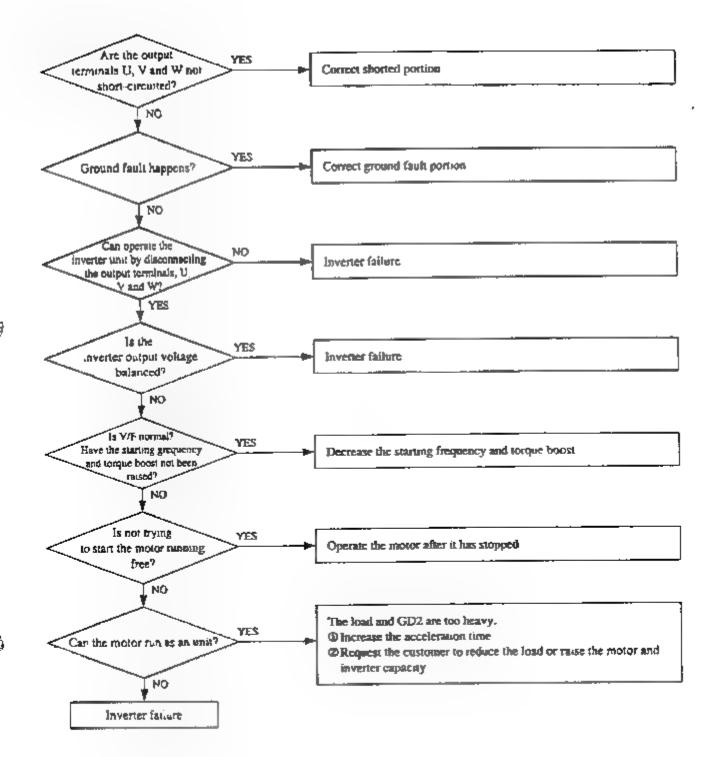
Select one item of the monitor mode. (Press the FUN key once, then press the MON key)



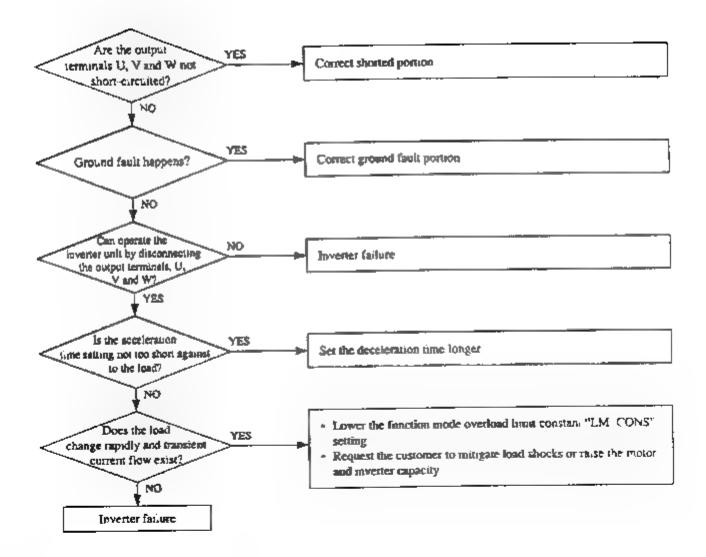


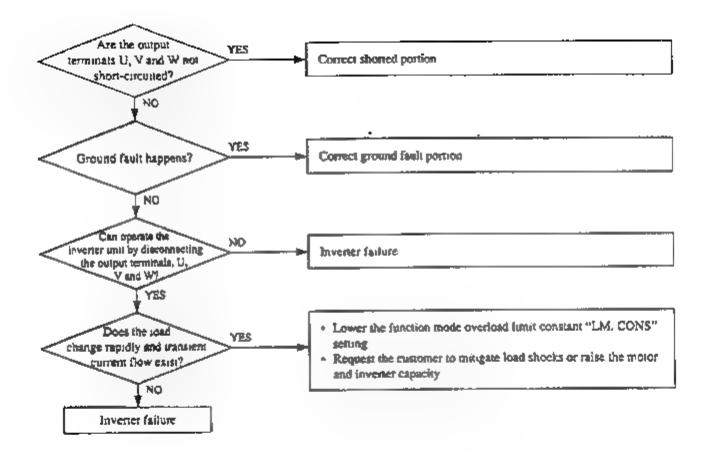
Check item	Correction
Is the STOP key on the digital operation panel not pressed while in the terminal mode?	Stop the operation command from the terminal mode once, then reinput the operation command
Is the DB command not inputted?	After turning the DB command off, input the operation command
Is the RS/FRS command not inputted?	After turning the RS/FRS command off, input the operation command
Is the frequency setting not 0?	Correct the frequency setting to a desired frequency
Is the display on the digital operation panel not the function mode?	Go to the monitor mode by pressing the MON key
Does trip happen?	Reset
If the frequency setting command mode (F-SET M) is "Terminal", does the speed command exist between terminals O and L, or OI and L on printed board?	Evaluate the speed command circuit
There is input on the printed board at multistage speed input terminals I and 2; but is the setting of "SPEED1" to "SPEED3" not made OHz?	Set "SPEED 1 to 3" to a desired frequency or cutoff the command to terminals 1 and 2
Is the command input not made externally by selecting the internal command (remote) mode or is the command input not made from the digital operation panel by selecting the external command (terminal) mode?	Check the operation mode. (Input the operation command with the mode currently set)
Are the external command (terminal) mode and the FW and RV terminals on the printed board not inputted simultaneously?	Be sure to allow either forward or reverse operation to function
Minimum frequency>Frequency set value?	Set the frequency to minimum frequency or above

(a) Overcurrent trip (PM. Accel) is actuated

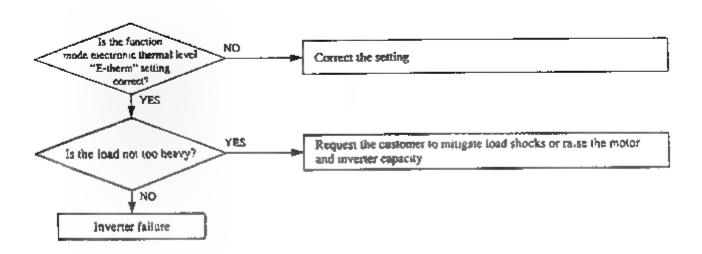


(b) Overcurrent trip (PM. Decel) is actuated

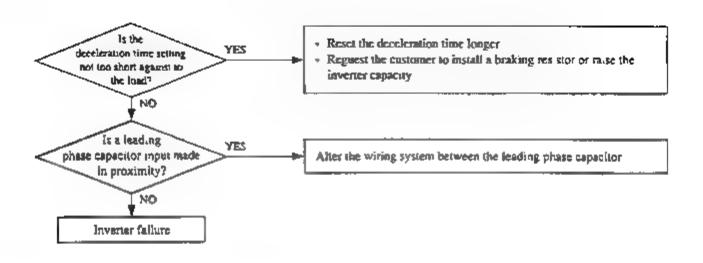




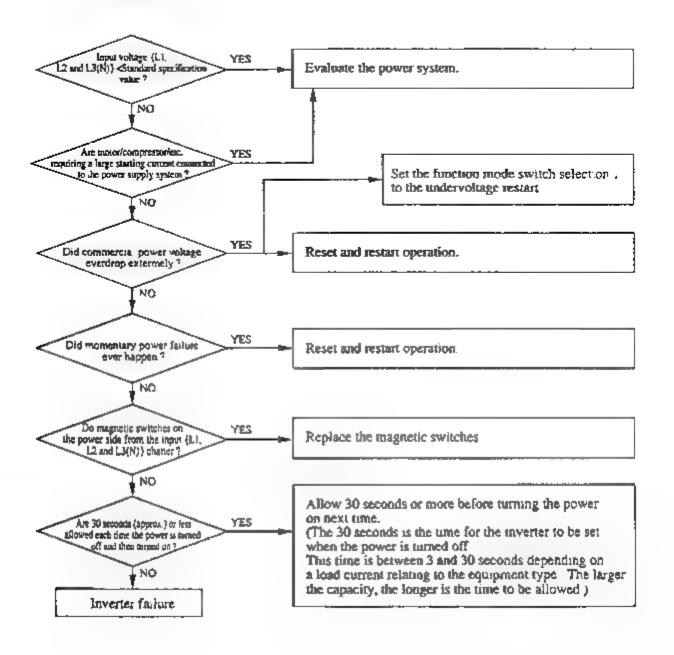
(d) Overload (OverL) is actuated



(e) Overvoltage trip (OverV) is actuated



(f) Undervoltage trip (UnderV) is actuated



(a) Phase failure

This inverter is not provided with the phase failure protection on the power supply; hence the following are expected to happen in the event of occurrence of the phase failure.

- The normal operation will continue when there is little load, whereas the main capacitor (CB) life shortens because of an increase in the CB ripple current.
- If a load is applied, the undervoltage and overcurrent protection is actuated.
- (b) Be careful of the following conditions because the converter module may be damaged.
 - When the power supply voltage unbalance ratio is 3% or above.
 - When the power supply capacity is ten times that of the inverter, and it is 500 kVA
 or greater.
 - · When a severe voltage transients occur

Examples: When multiple inverters are connected to a short bus.

When a leading phase capacitor is turned on/off.

In the cases above, it is recommended that a reactor of about 3 percent of the power supply voltage (voltage drop at the rated current) be inserted on the power supply.

4. MEASUREMENT AND ADJUSTMENT OF CONTROL CHARACTERISTICS

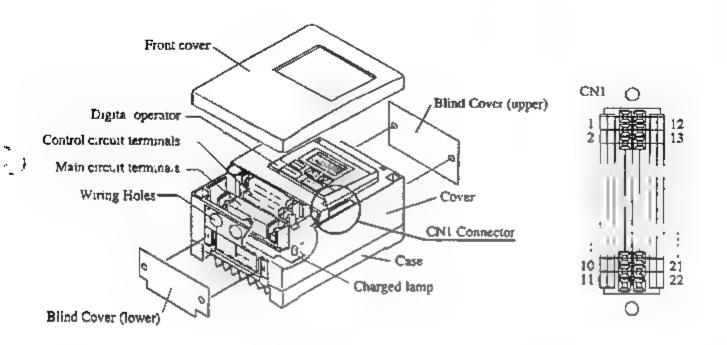
4.1 Controlled Power Supply Voltage

Description	Allowable voltage range (V)		ent location		
	A 100 100 100 100 100 100 100 100 100 10	E. ST.	- TAK. "		
PV5 (for +5 V use) power supply	4.9 to 5.2	CN1 (12)	CN1 (13)		
NV12(for -12 V use) power supply (NOTE)	-10 8 to -15.0	CN1 (2)	CN1 (13)		
	004 to 015SF				
PV24 (for +24 V) power supply	(for +24 V) power supply 21 6 to 30 0				
	022SF, 015 to 037HF				
	21.6 to 26.4				
VDC voltage (for detection of DC bus voltage)	200 V class 24.3 to 25.9 V at 300 VDC	CN1 (1)	CN1 (13)		
	400 V class 24.3 to 25.9 V at 600 VDC				

NOTE: With the remote operator connected

* CN1s shown above are for the right hand side connector of the main body.

(See the picture at the bottom).



Appearance of J100 (example: 004SF)

CN1 connector

Make detections of the following by varying the main circuit voltage $\{LI, L2 \text{ and } L3(N)\}$ which have been adjusted with the variable transformer. When checking the BRD function, connect a resistance of hundreds $k\Omega$ between P and RB.

Descr	ipt.on	. Model	Detected voltage (between P and N on main circuit)	Criteria				
BRD	ON	004 to 022SF	330 to 350 VDC or above	Connect an oscilloscope from RB to P (+). The voltage waveform must be-				
		015 to 037HF	660 to 700 VDC or above	come "L".				
!	OFF	OFF	OFF	OFF	OFF	004 to 022SF	330 to 350 VDC or below (When V-SET is set to 200V)	Connect an oscilloscope from RB to P
		015 to 037HF	660 to 700 VDC or below (When V-SET is set to 400V)	10111-				
OV-T	TRIP 004 to 022SF		375 to 405 VDC	The output power turned off; Logic PCB:				
		0.5 to 037HF	750 to 810 VDC	Control circuit check round PU ← control circuit terminal L must be 'H LEVEL				
				Alarm relay output: ALO-AL1 ON → OFF when B contact is selected				
				- Overvoltage trip display "?ERROR Over V" (Remote operator display)				

NOTE 1: Use care when checking BRD function, because measurement of high voltage is involved.

NOTE 2: Since there is no N(-) terminals on the main circuit, use P terminal and () pin on the diode module (DM).

4.3 Overcurrent (Overload) Detection Characteristics

With the motor running, gradually increase the load and then make the following measurement.

Description	Method to lest	Criteria	Renarks
Overload limit level	① F-24 LM.CONS 50% 01.0 setting: Apply a load of 40 to 60 percent of the inverter rated cur- rent. ② F-24 LM.CONS 150% 01.0 setting. Apply a load of 140 to 160 percent of the inverter rated cur- rent.	Output frequency must start decreasing.	Possible to change LM.CON setting with the remote operator connected
Overload trip	Apply a load of 180 percent of the inverter rated current.	 Overload trip must occur in about 10 to 20 seconds The output power turned of. {CN1(13) to CN1(20)}; "H" level) Alarm output (AL0-AL1 ON → OFF).* Overload trip display: (?"ERROR Over, L." (Remote operator display)) 	Impossible to limit overloading with LM CONS 150% 31.0 setting (Use the remote operator)
Overcurrent trip	Apply a load of 200 to 220 percent of the inverter rated current.	 The output power turned off> {CN1(13) to CN1(20)}; "H" level) Alarm output (AL0-AL1 ON → OFF).* Overcurrent trip display ("ERROR OC. Drive" (Remote operator display) 	Impossible to limit overloading with LM.CONS 150% 31.0 setting (Use the remote operator.)

^{*} When B contact is selected for alarm relay output.

4.4 Undervoltage Detection Characteristics

Operate the inverter at the maximum output frequency. As the main circuit power supply voltage {L1, L2 and L3(N)} is gradually decreased through the variable transformer, the output has to be turned off or Undervoltage trip must occur (this operation to be carried out at the rated load).

Description	Model	Operating voltage (Input voltage)	Criteria
Undervoltage	004 to 022SF	140 to 160 VAC	 The output power turned off. (CN1(13) to CN1(20)); "H" level)
	015 to 037HF	280 to 320 VAC	 Alarm output: AL0-AL1, ON → OFF When b contact is selected. Undervoltage display: ("?ERROR Under. V")

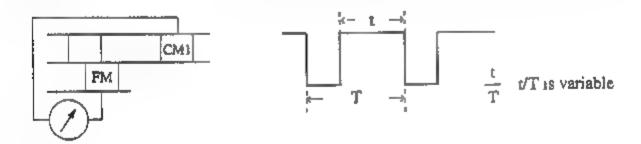
Following the detective operation, execute a latching. After resetting (by short-circuiting RS to L, or pressing the [STOP] key,) release the latching.

4.5 Forced Resetting Characteristics

Description -	Operation	Criteria
Forced resetting	Short circuit RS to PV24 on the printed board.	The abnormal mode must be rest.

4.6 External Frequency Indicator (Analog Meter) Adjustment Characteristics (Monitor Mode M-ADJ)

Connect the remote operator. Select [F 21 SWITCH] with the function mode, and set to [SWITCH FM ANA] In this state, an output (t/T) which is proportional to the output frequency is available between FM- on the printed-circuit board. Adjust the M-ADJ constant on the monitor mode so the meter reading becomes maximum at the maximum frequency.



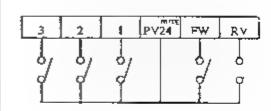
4.7 How to return to the initial setting (Setting when shipped from the factory)

Follow the following procedure when it is necessary to return to the initial setting made the factory

- The Short circuit FW, RV and I terminal to PV24 on the control circuit terminal
- Tutn the power on.
- Make the FW, RV and 1 terminal open when 0 0 0 is displayed on the digital operator.
 (The remote operator display FS000. 0HZ 000. 0HZ)
- Turn the power off after six seconds or more have past.
 (If the power is turned off within six seconds, the initial setting may be ineffective)

NOTE: The following table shows how to return to the initial setting for Europe, Japan and US version.

Version Vo	F₩	RV	1	2	3	Setting for	
Europe	Close	Close	Close				
US	Close	Close	Close	Close		Factory setting	
Јарап	Close	Ciose					
Europe	Close	Close			Close		
US	Close	Ciose	Close	Close	Close	Factory setting and error clear	
Japan	Close	Close		Close	Close		

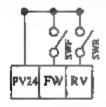


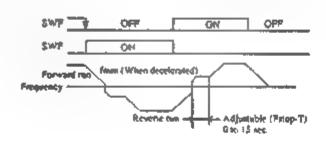
NOTE: J100-E series should be CM1, PV24 is for J100-E2 series.

5. OPERATIONAL ADJUSTMENT

5.1 Motor noload, forward and reverse operations

The motor must accelerate without allowing the OC-TRIP to occur, when the motor is run forward first, and then with the use of the selection switch, the speed is decreased and the operation is switched over to the reverse run.





:t ;:x :x:.

5.2 Motor operation with load (100% load)

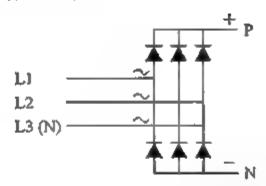
Description 🗼	Operation = '	Criteria
Balance of output vo.tage and current	Make measurements of the voltage and current at the inverter output (U, V and W). (See Section 7)	 Free from open phase, etc Balanced output voltage and current Motor operation free from abnormal noises
Corrent monitor	Check the output current values on the operation monitor (F1).	90 to 110% of the inverter rated current (approx.)
Voltage monitor	Check the DC voltages values on the operation monitor (F1). Check the DC voltages (Vpn).	The value depends on input voltages [004 to 022 SF] 270 volts (approx.) against the 200 VAC input [015 to 037 HF] 540 volts (approx.) against the 400 VAC input

 [#] J100-E series should be CM1.
 PV24 is for J100-E2 series.

6. ACTIONS TO TAKE IN AN OCCURRENCE OF ABNORMALITIES

6.1 How to check the converter module

Possible to check the module with the tester



Circuit diagram of the converter module

Turn the power off and start the work after the DC bus voltage has become 15 volts or below. Use the 1Ω range when making measurements with the tester. (A sample way to check the module as assembled)

Jester terminal	Resistance value
Three different ways for	50kΩ or above
P(+) →Each of L1, L2 and L3 (N)	50kΩ or above
Each of L1, L2 and L3 (N) → P(+)	50kΩ or below
N() →Each of L1, L2 and L3 (N)	50kΩ or below
Each of L1, L2 and L3 (N) → N(-)	50kΩ or above

 AC terminal (L1, L2 and L3 (N))

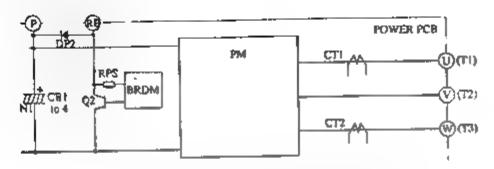
NOTE: Since there is no N(-) terminals on the main circuit terminal, use the following terminals when making the measurements.

- 004 to 007SF: N(-) pin at the diode module (DM) connection
- 015 to 022SF, 015 to 037HF: N connector at the capacitor PCB connection

Replace the converter module if any of the measurements is out of the values shown above.

 Trouble to occur when the converter module fails: MCB trip (Short-circuit of the power supply) 6.2 How to check the FWI module and manistrois

Check the module with the tester



Inverter module circuit diagram (004 to 022SF)

Turn the power off and start the work after the DC bus voltage has become 15 volts or below. Use the 1Ω range when making measurements with the tester. (A simple way t check the module as assembled)

NOTE: There is a possibility that the module is out of order even when judged to be standards

Tester terminal	Resistance value	Location to check:
$P \rightarrow U$		U phase upper arm
$P \rightarrow V$	$50 \mathrm{k}\Omega$ or below	V phase upper arm
$P \rightarrow W$		W phase upper arn
N(-) → U		U phase upper am
N(-) → V	50kΩ or above	V phase upper arr
N(-) → W		W phase upper ar
$U \rightarrow P$		U phase upper ar
$V \rightarrow P$	50kΩ or below	V phase upper ar
$W \rightarrow P$		W phase upper a
$U \rightarrow N(-)$		U phase upper a
$V \rightarrow N(-)$	50kΩ or above	V phase upper a
$W \rightarrow N(-)$		W phase upper
$N(-) \rightarrow RB$	50Ω or below	BRD transistor (004
$RB \rightarrow N(-)$	50kΩ or above	Divid damaiator (00+
$N(-) \rightarrow RB$	50kΩ or above	BRD transistor with
$RB \rightarrow N(-)$	50kΩ or above	to 037HF)

6-2

6,3

6.4

Se co

> Se: pre

- NOTE. Since there is no N() terminals on the main circuit terminal, use the following terminals when making the measurements.
 - 004 to 007SF: (-) pin at the diode module (DM) connection
 - 015 to 022SF, 015 to 037HF: N connector at the capacitor PCB connection

Troubles to occur:

When the PM module fails:

The OC trip occurs even when the motor is not connected.

When the BRD transistors (BRD transistors within PM) fail:

• BRD discharge resistor heats up

6.3 How to check the printed-circuit board

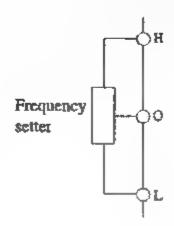
Visually check the mounted printed-circuit board for damaged resistor and thick-film module (called BRDM (004 to 022SF)/PSM), short-circuited IC lead due to deposited foreign matter and abnormal or disconnected connector.

6.4 How to check the frequency setting digital

Select "01" for F9 with [FUNC] key and key on the digital operation panel, and connect the frequency setter betweenthe terminals O and L on the printed circuit board. Set the frequency to a maximum, select F1 with the [FUNC] key on the operation panel and press the [RUN] key. (In the case of the remote operator, select "Terminal" for F SET-M)

Be sure to check if the frequency can be set to a maximum setting value. (Example)





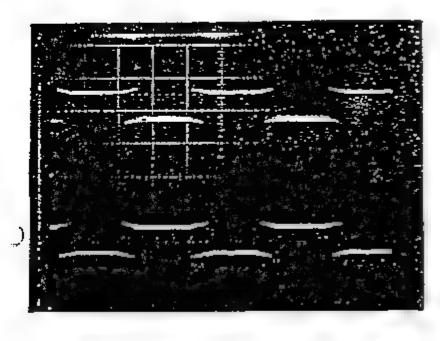
6.5 How to check control signals

While operating the inverter, PWM waveforms appear between the connector CN1(20) p.n(U) or CN1(18) pin(X) and CN1(13) pin (GNDA). Note that these PWM waveforms are different depending on the frequency setting.

When the PWM waveform is incorrect, replace the control board.



- CN1(20) pin ← CN1(13) pin CN1(20) pin ← CN1(13) pin (U ← GNDA) (X ← GNDA)
- CN1(19) pin ← CN1(13) pin CN1(17) pin ← CN1(13) pin (V ← GNDA) (Y ← GNDA)
- CN1(8) pin ← CN1(13) pin CN1(7) pin ← CN1(13) pin (W ← GNDA) (Z ← GNDA)



f=60Hz

CN1(20) pin ← CN1(13) pin (U) (GNDA)

0V

CN1(18) pin ← CN1(13) pin (X) (GNDA)

0V

2V/div 5ms/div

7.1 Maintenance and inspection precautions

(1) Precautions before starting maintenance and inspection

Be sure to confirm the following before starting maintenance and inspection because there is a danger of receiving an electric shock.

:38:

- The display on the digital operation panel has turned off and the charge lamp on the PCB has gone out (J100-022SF takes about 4 minutes for example).
- The DC bus voltage is 15V or below when measured with the tester.

(2) General precautions

Always keep the unit clean so that dust or other foreign matter does not enter the inverter. Use special care with respect to broken lines and faulty connections. Firmly connect terminals and connectors. Keep electronic equipment away from moisture and oil. Dust, steel filings and other foreign matter can damage insulation, causing unexpected accidents, so take special care.

7.2 Measurement of input/output voltage, current and power

General measuring instruments for input/output voltage, current and power are shown in Figure 7.3 and Table 7.1.

The voltage to be measured is the fundamental wave effective value and the power to be measure is the total effective value.

(1) Measurement of output voltage

The moving iron type instrument does not give accurate readings for measurement of the output voltage. Make measurements according to the method shown in Figure 7.3 (Table 7.1) or using the circuits indicated in Figures 7.1 and 7.2.

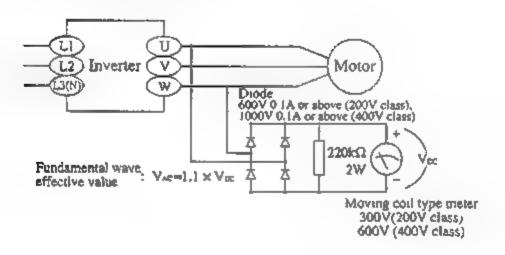


Figure 7.1 Output voltage measurement circuit

When a load is not connected to the output terminals U, V and W, voltage is present at them because of the leakage current of semiconductors (about 2mA) even when the output frequency command is made naught.

When connecting the voltmeter to the output terminals under the conditions mentioned above, make connections as indicated in Figure 7.2 to prevent the indication error of the meter

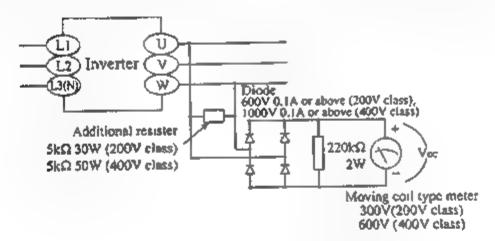


Figure 7.2 Output voltage measurement circuit

(2) Measurement of input voltage and input/output current

Make measurements of the input voltage and input/output current for all of the three phases with the moving-iron type meter (See Figure 7.3 and Table 7.1).

(3) Measurement of input/output power

Make measurements of the input/output power with the electrodynamic type waltmeter for single phase use. Make measurements for all of the three phases in cases where there is an unbalance in voltages and currents.

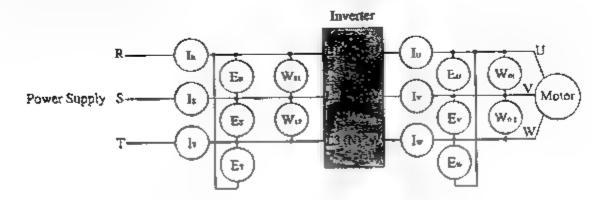


Figure 7.3 Parts to be measured

Table 7.1 Measuring instruments

Measuring item	Paris to be measured.	Measuring instrument 💥 🖑	Remarks
Supply vo.tage E	Between L1 and L2, L2 and L3(N), L3(N) and L1 (ER-S)(E-ST)(ET-R)	Moving from type voltmeter or rectifier type voltmeter	Pundamental wave offective value
Supply current I1	Amperage at L1, L2 and L3(N) (IR)(Is)(IT)	Moving-non type ammeter	Total effective value
Supply power W1	Between L1 and L2, and L2 and L3(N) (W11)(W12)	Electrodynamic type waterneter	Total effective value
Supply power factor Pf1	Calculate the supply power. It and supply power Wi	factor from the measured supply voltage, $Pf_1 = \frac{W_1}{\sqrt{3} \cdot E_1 \cdot I_1} \times 100(\%)$	E1, supply current
Output voltage E0	Between U and V, V and W, W and U (Eu-v) (Ev-w) (Ew-u)	Refer to Figure 7-2 or recufier moving-iron type ammeter	Total effective value
Output current Id	Amperage at U, V and W (IU)(IV)(IW)	Moving-iron type ammeter	Total offective value
Output power Wo	Between U and V, V and W (W01)(W02)	Electrodynamic type wattmeter	Total effective
Output power factor Pfo	Cakulate the output power soutput power Wo	Factor from the output voltage E0, output $Pfo = \frac{Wo}{\sqrt{3} \cdot Eo \cdot Io} \times 100(\%)$	current Io and

- NOTE 1 Use a meter indicating a fundamental wave effective value for voltage, and meters indicating total effective values for current and power.
- NOTE 2 Since the inverter output waveform is a distorted wave, the measuring instruments shown in the table above are hable to cause errors at low frequencies. The measuring method and instruments indicated above provides comparatively accurate values.

 Some testers (general purpose products) are not applicable to the distorted wave
- NOTE 3. For the input and output current, measurements of the power are made with the digital power meter, e.g., YEW 2503 and 2504.

7.3 Method to Measure institution Resistance and Willistant Voltage

Make these measurements and tests by short-circuiting the terminals as shown in Figure 7.4, and by following the conditions described.

[Conditions]

a) Make insulation resistance measurements between the terminals and grounding with the 500DCV megachm-meter, and make sure that 5 M ohms or greater is indicated.

b) Withstand voltage test

Make withstand voltage tests by supplying 1500VAC (200V class), or 2000 VAC (400 V class) to between the terminals and grounding for one minute, and make sure that there are no abnormalities.

Do not conduct withstand voltage tests for terminals other than those indicated in Figure 7-4.

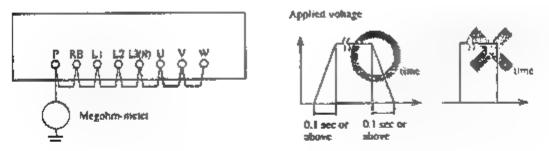


Figure 7.4 Insulation Resistance Tests and Withstand Voltage Tests

7.4 Maintenance of parts

- (1) Printed-circuit boards are maintenance-free under normal applications. However, in cases in which maintenance and inspection are necessary, pay attention to the prevention of damage caused by static electricity as shown below, and be sure to follow the instructions in Section 4. MEASUREMENT AND ADJUSTMENT OF CONTROL CHARACTERISTICS and Section 5. OPERATIONAL ADJUSTMENT.
 - · Prevent damage caused by static electricity

The MCUs and LSIs on a printed-circuit board can be destroyed by static electricity, so be sure to ground work benches, soldering irons, and yourself before working on a printed-circuit board.

(2) Maintenance of smoothing capacitor and cooling fan

We recommend that smoothing capacitors CB and cooling fans be regularly replaced every three years taking their lives into account. Note that their lives shorten when they are used, in particular, under high temperatures and heavy loads.

Appendix 1 J100 series logic and main circuit elementary wiring diagram

ELYPE	Logic diagram No	Majo cucul diagram No.
J100-004SFE2	2T800924	3T813644
J100-0075FE2	Ditto	Ditto
J100-015SFE2	Ditto	3T813643
J100-022SFE2	Ditto	Ditto
J100-015HFE2	Ditto	3T813642
J100-022HFE2	Ditto	Ditto
J100-037HFE2	Ditto	Ditto

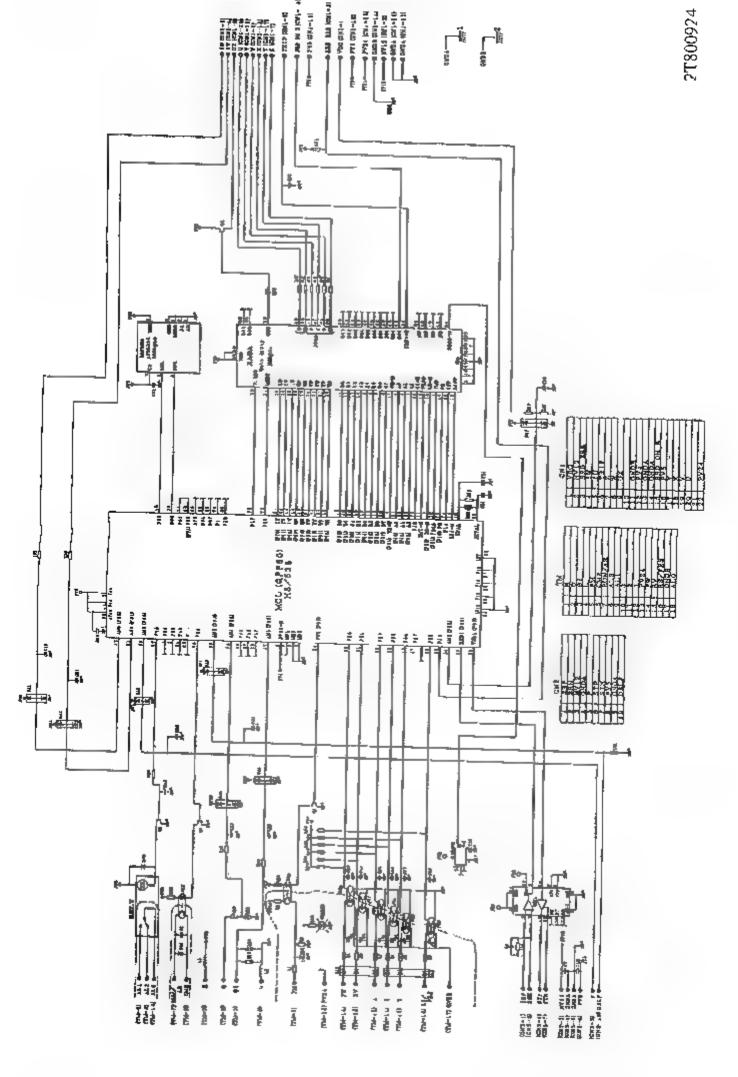
Appendix 2 J100 series structural drawing

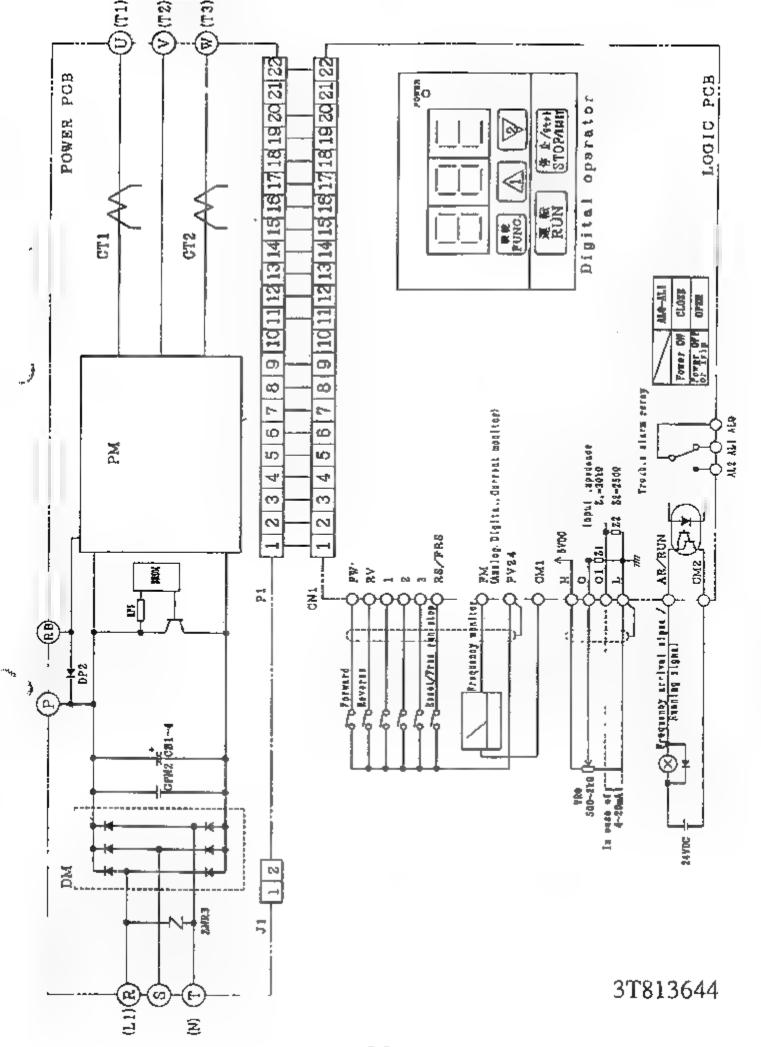
1002 A SP	Structural drawing No.
J100 004SFE2	3T810495
J100 007SFE2	3T810496
J100-015SFE2	3T810498
J100-0228FE2	Ditto
J100-015HFE2	3T810499
J100-022HFE2	Ditto
J100 037HFE2	Ditto

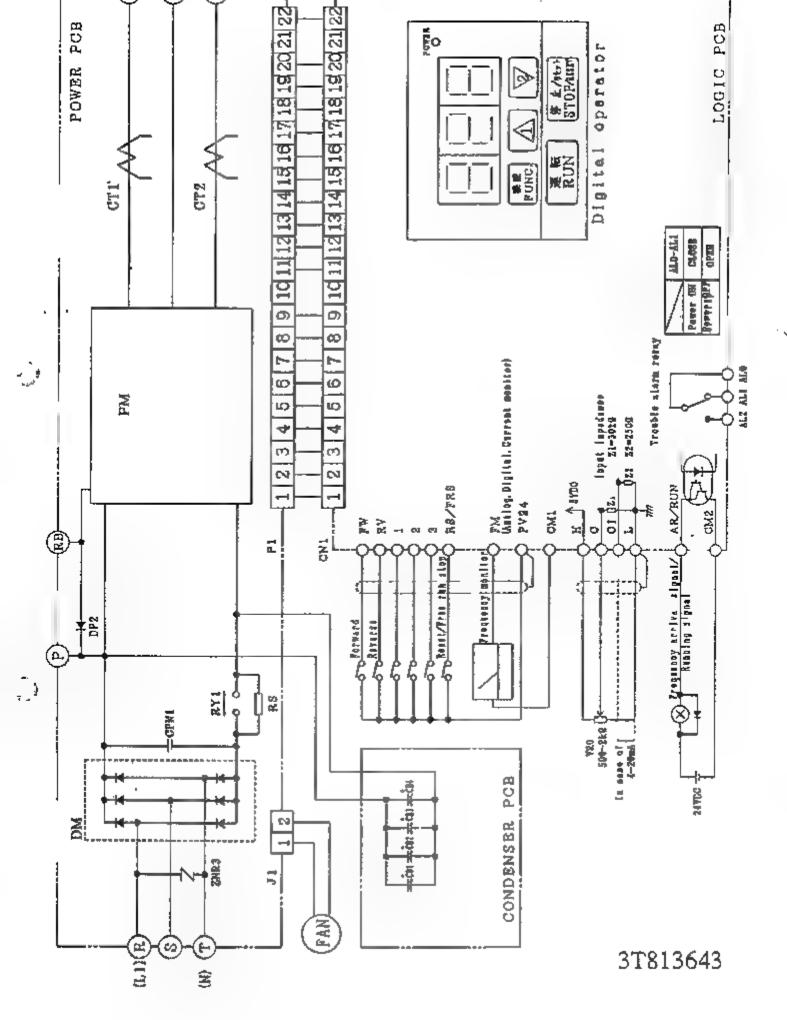
_					NE
	Content	ernates1 symbol	J100 series	Commission of the Commission o	VWS-9 senes
3	Run/stop command Forward/ reverse run com- mand	H O L FW RV	Electrically insulated	H O L FW RV	Make frequency settings with the monitor mode, and select operation stop command with the "Terminal" mode. ***********************************
	Frequency command (voltage input)	O L	* At fmax with 5V or 10V between O and L (Select with remote operator switch selection 2⑤) ① Voltage between H and L: 5V ② Possible to use up to 500 to 2 kΩ of VR ₀ * Output frequency gain-bias function for the analog command between O and L is possible to set with the remote operator function mode F-18 (F-START) and F-19 (F-END). The maximum frequency adjustment is possible with F-01 (Fmax).	L	* At fmax with 10V between O and L ① Voltage between H and L 10V (AVR used) ② Possible to use up to 500 to 2 kΩ of VR ₀ with a built-in AVR * Output frequency gain-bias function for the analog command between O and L is possible to set with the digital operation panel function mode F-26 (F START) and F-27 (F END). The maximum frequency adjustment is possible with F-3 (+fmax).

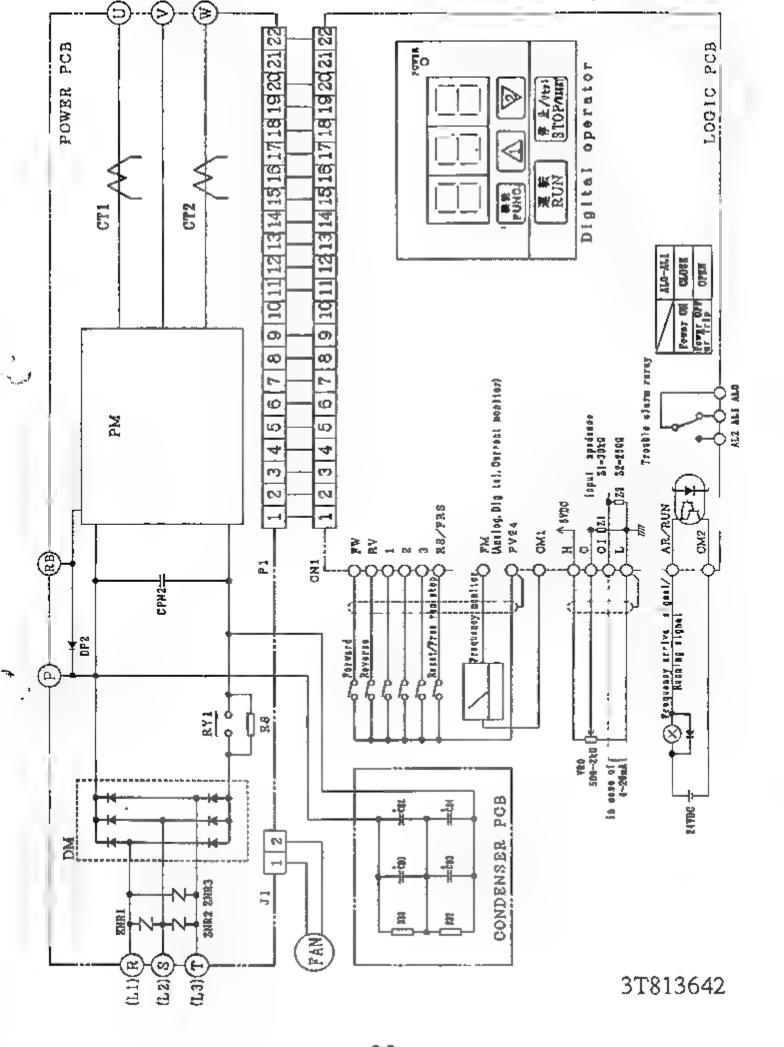
Content	Tecta made symptox	J100 series	Tomics symbol	VWS-3, series
Frequency command (current input)	OI ·	Make selections with switch selection 2 ⑦ of the remote operator. 4 to 20mA Of	L	Make selections with switch selection 2 ⑦ of the remote operator 4 to 20 mA 101 0 to 5 v L (0 to 10 v) * Input impedance 250Ω Adjustable 4 to 20 mA 0 to 5 v 0 to 10 v Input/output characteristic * Input impedance 250Ω Selection of the start point (F-START) and end point (F-END) of the frequency for 4 to 20 mA is possible with the function mode F-26 and F-27. The inversion of the two-dot chain line is also possible.
	1		I	

	Content	Terrahas)	J100 series	Townsol symbol	VWS 3 series
•	Frequency	FM CM1	Possible to monitor the inverter output frequency or output current. The output frequency monitor provides either analog or digital signal output. The output current monitor provides an analog signal only. Make selections with switch selection 1 ② and 4 ⑤. * Analog grequency monitor signal ① Analog meter specification: 10 V. ImA full scale. ② A digital duty control signal proportional to the output frequency is outputted as a maximum frequency full scale. ③ Meter reading adjustment is possible with M-ADJ of the monitor mode. * Digital frequency monitor signal (for the use of frequency counters) ① The output duty of a pulse series with the same frequency as the output frequency is about 50%. * Analog current monitor signal A digital duty control signal proportional to the output current is outputted as a 200% full scale of the inverter rated output current.	FM L	Possible to use those for the use of analog meters and digital frequency counters with the same terminal Make selections at the digital operation panel with the function mode F-28 switch 3 * For analog meter use ① Analog meter specification: 10V, ImA full scale. Internal resistance: 10 to 22 k\(\Omega\). ② Signal for analog meter use Digital duty control signal ③ Meter reading adjustment is possible with M-ADJ. (Provided if the meter's internal resistance is 10 to 22 k\(\Omega\)) * When the digital monitor (for frequency counter use) is selected * Output signal is as indicated be low.
	Alarm output contact	AL AL	I in normal state: AL0-AL1 Closed	AL AL	

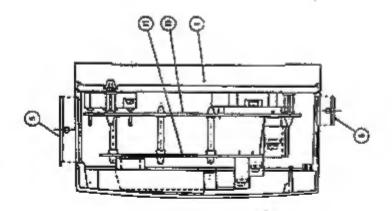


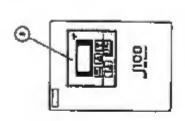


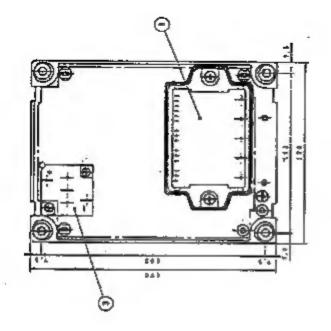


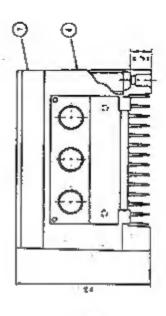


	-	1	1	. 1	-	-	-	-		-	-		-
A Property of	Diecesi case	Inverter module	Diode module	Cover	Blind corer (upper)	Bilns cover (lower)	Pront cover	Digital operation punct		Power PCB	Logic PCB		Per
22	S	PM	DW	S	CV	CV	2	PANEL.		PC9	PCB		FAN
8.0	-	2	5	-	90		-		6	0	=	23	52

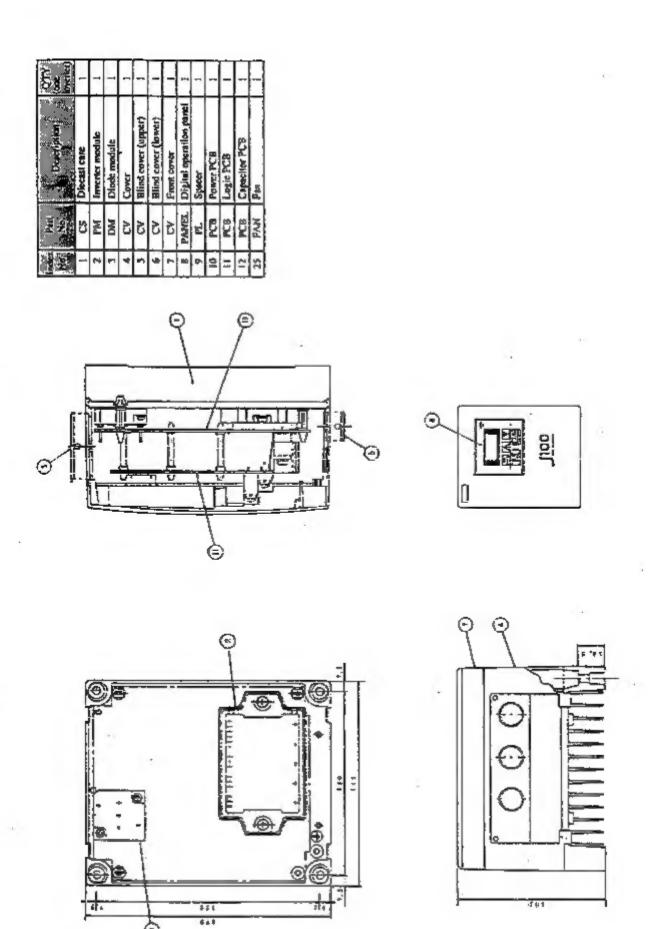




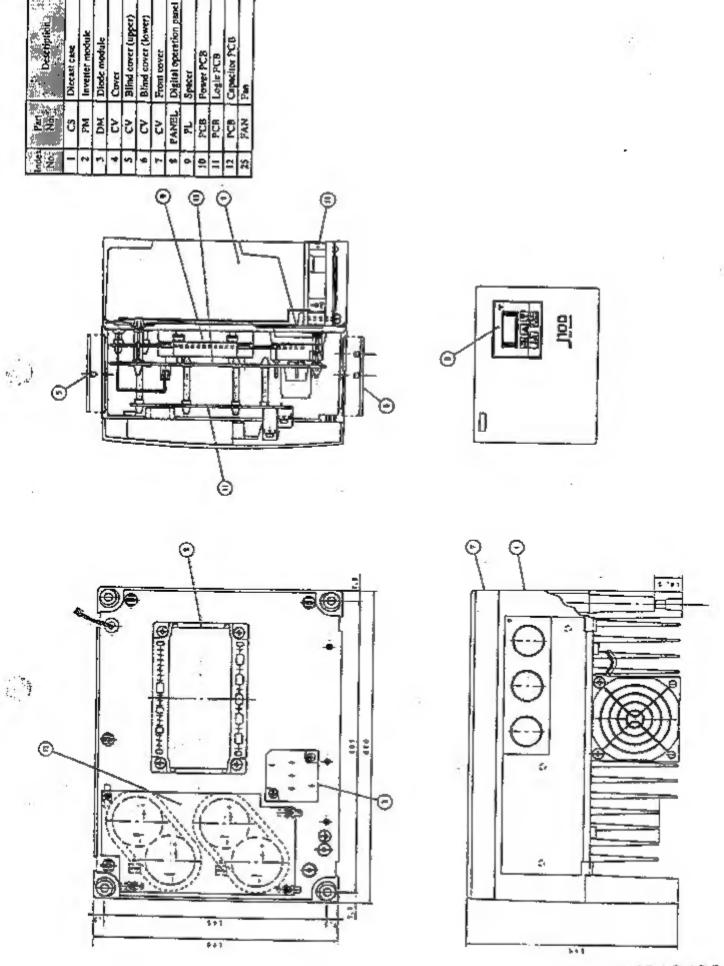




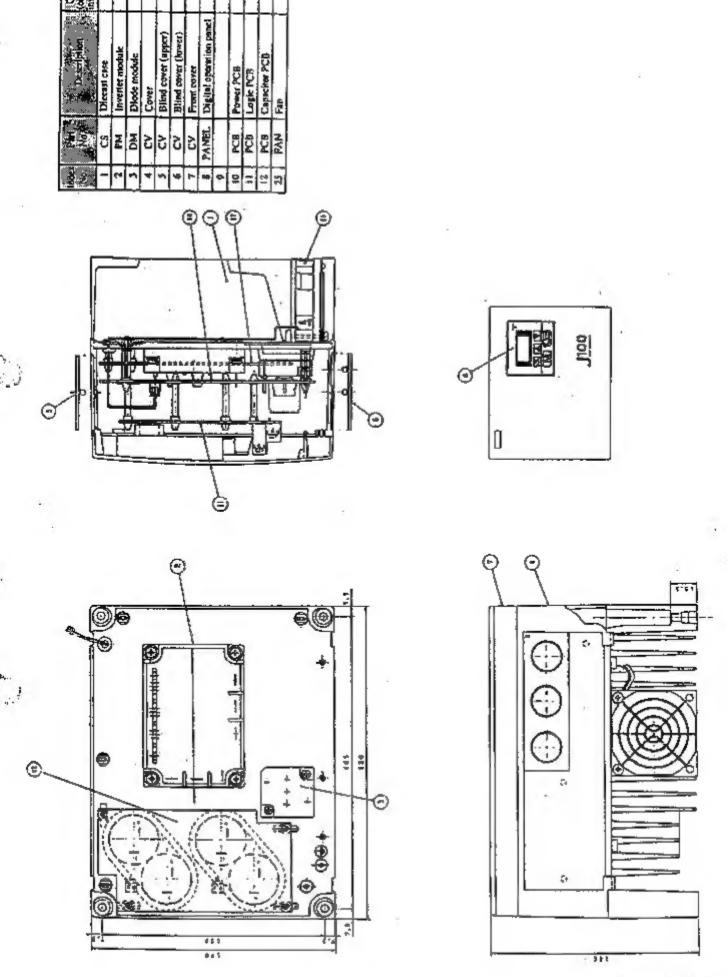
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